

NOTE:

The 2-digit display is located on the motherboard rear I/O. The following list of codes will assist in troubleshooting your PC.

Checkpoints**Security (SEC) Phase**

Status Code	Description
01	Power on. Reset type detection (soft/hard).
02	AP initialization before microcode loading
03	North Bridge initialization before microcode loading
04	South Bridge initialization before microcode loading
05	OEM initialization before microcode loading
06	Microcode loading
07	AP initialization after microcode loading
08	North Bridge initialization after microcode loading
09	South Bridge initialization after microcode loading
0A	OEM initialization after microcode loading
0B	Cache initialization
0C – 0D	Reserved for future AMI SEC error codes
0E	Microcode not found
0F	Microcode not loaded

Pre-EFI Initialization (PEI) Phase

Status Code	Description
10	PEI Core is started
11	Pre-memory CPU initialization is started
12	Pre-memory CPU initialization (CPU module specific)
13	Pre-memory CPU initialization (CPU module specific)
14	Pre-memory CPU initialization (CPU module specific)
15	Pre-memory North Bridge initialization is started
16	Pre-Memory North Bridge initialization (North Bridge module specific)
17	Pre-Memory North Bridge initialization (North Bridge module specific)
18	Pre-Memory North Bridge initialization (North Bridge module specific)
19	Pre-memory South Bridge initialization is started
1A	Pre-memory South Bridge initialization (South Bridge module specific)
1B	Pre-memory South Bridge initialization (South Bridge module specific)
1C	Pre-memory South Bridge initialization (South Bridge module specific)
1D – 2A	OEM pre-memory initialization codes
2B	Memory initialization. Serial Presence Detect (SPD) data reading
2C	Memory initialization. Memory presence detection
2D	Memory initialization. Programming memory timing information

2E	Memory initialization. Configuring memory
2F	Memory initialization (other).
30	Reserved for ASL (see ASL Status Codes section below)
31	Memory Installed
32	CPU post-memory initialization is started
33	CPU post-memory initialization. Cache initialization
34	CPU post-memory initialization. Application Processor(s) (AP) initialization
35	CPU post-memory initialization. Boot Strap Processor (BSP) selection
36	CPU post-memory initialization. System Management Mode (SMM) initialization
37	Post-Memory North Bridge initialization is started

Driver Execution Environment (DXE) Phase

Status Code	Description
60	DXE Core is started
61	NVRAM initialization
62	Installation of the South Bridge Runtime Services
63	CPU DXE initialization is started
64	CPU DXE initialization (CPU module specific)
65	CPU DXE initialization (CPU module specific)
66	CPU DXE initialization (CPU module specific)
67	CPU DXE initialization (CPU module specific)
68	PCI host bridge initialization
69	North Bridge DXE initialization is started
6A	North Bridge DXE SMM initialization is started
6B	North Bridge DXE initialization (North Bridge module specific)
6C	North Bridge DXE initialization (North Bridge module specific)
6D	North Bridge DXE initialization (North Bridge module specific)
6E	North Bridge DXE initialization (North Bridge module specific)
6F	North Bridge DXE initialization (North Bridge module specific)
70	South Bridge DXE initialization is started
71	South Bridge DXE SMM initialization is started
72	South Bridge devices initialization
73	South Bridge DXE Initialization (South Bridge module specific)
74	South Bridge DXE Initialization (South Bridge module specific)
75	South Bridge DXE Initialization (South Bridge module specific)
76	South Bridge DXE Initialization (South Bridge module specific)
77	South Bridge DXE Initialization (South Bridge module specific)

78	ACPI module initialization
79	CSM initialization
7A – 7F	Reserved for future AMI DXE codes
80 – 8F	OEM DXE initialization codes
90	Boot Device Selection (BDS) phase is started
91	Driver connecting is started
92	PCI Bus initialization is started
93	PCI Bus Hot Plug Controller Initialization
94	PCI Bus Enumeration
95	PCI Bus Request Resources
96	PCI Bus Assign Resources
97	Console Output devices connect
98	Console input devices connect
99	Super IO Initialization
9A	USB initialization is started
9B	USB Reset
9C	USB Detect
9D	USB Enable
9E – 9F	Reserved for future AMI codes
A0	IDE initialization is started
A1	IDE Reset
A2	IDE Detect
A3	IDE Enable
A4	SCSI initialization is started
A5	SCSI Reset
A6	SCSI Detect
A7	SCSI Enable
A8	Setup Verifying Password
A9	Start of Setup
AA	Reserved for ASL (see ASL Status Codes section below)
AB	Setup Input Wait
AC	Reserved for ASL (see ASL Status Codes section below)
AD	Ready To Boot event
AE	Legacy Boot event
AF	Exit Boot Services event
B0	Runtime Set Virtual Address MAP Begin
B1	Runtime Set Virtual Address MAP End
B2	Legacy Option ROM Initialization
B3	System Reset
B4	USB hot plug
B5	PCI bus hot plug
B6	Clean-up of NVRAM
B7	Configuration Reset (reset of NVRAM settings)
B8 – BF	Reserved for future AMI codes
C0 – CF	OEM BDS initialization codes

PEI Error Codes

Status Code	Description
50	Memory initialization error. Invalid memory type or incompatible memory speed

51	Memory initialization error. SPD reading has failed
52	Memory initialization error. Invalid memory size or memory modules do not match.
53	Memory initialization error. No usable memory detected
54	Unspecified memory initialization error.
55	Memory not installed
56	Invalid CPU type or Speed
57	CPU mismatch
58	CPU self-test failed or possible CPU cache error
59	CPU micro-code is not found, or micro-code update has failed
5A	Internal CPU error
5B	reset PPI is not available
5C	PEI phase BMC self-test failure

DXE Error Codes

Status Code	Description
D0	CPU initialization error
D1	North Bridge initialization error
D2	South Bridge initialization error
D3	Some of the Architectural Protocols are not available
D4	PCI resource allocation error. Out of Resources
D5	No Space for Legacy Option ROM
D6	No Console Output Devices are found
D7	No Console Input Devices are found
D8	Invalid password
D9	Error loading Boot Option (LoadImage returned error)
DA	Boot Option is failed (StartImage returned error)
DB	Flash update is failed
DC	Reset protocol is not available
DD	DXE phase BMC self-test failure

ACPI/ASL Checkpoints

Status Code	Description
01	System is entering S1 sleep state
02	System is entering S2 sleep state
03	System is entering S3 sleep state
04	System is entering S4 sleep state
05	System is entering S5 sleep state
10	System is waking up from S1 sleep state
20	System is waking up from S2 sleep state
30	System is waking up from S3 sleep state
40	System is waking up from S4 sleep state
AC	System has transitioned into ACPI mode. Interrupt controller is in PIC mode.
AA	System has transitioned into ACPI mode. Interrupt controller is in APIC mode.